



MEMORY EFFICIENT OCCURRENCE MODEL DESIGN FOR  
VLSI CAD

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REPLACEMENT SHEET

2/6

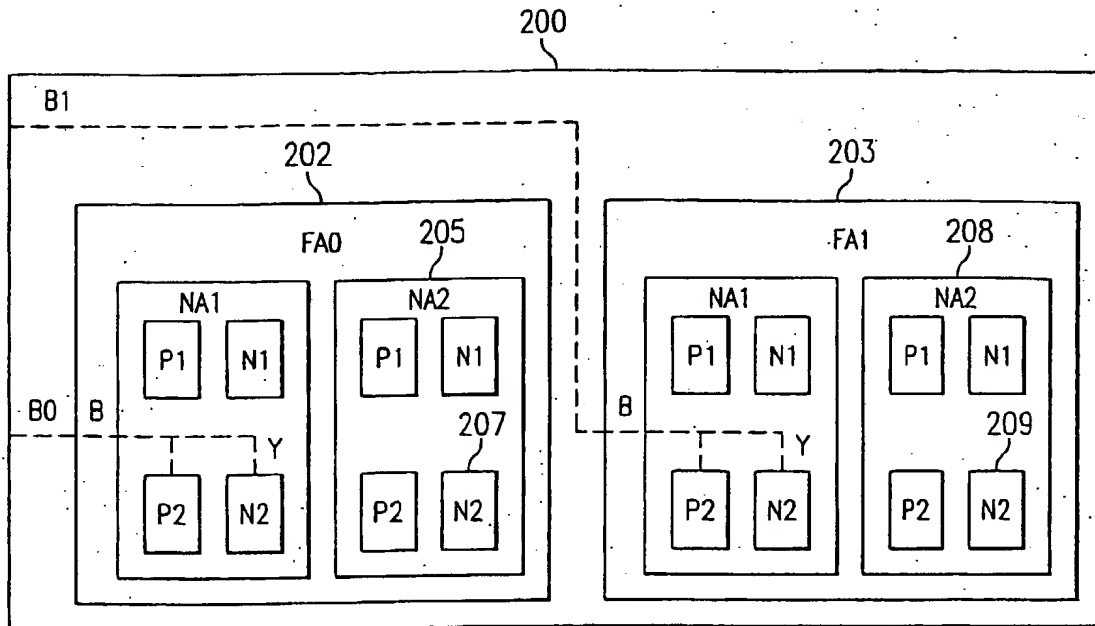


FIG. 3  
(PRIOR ART)

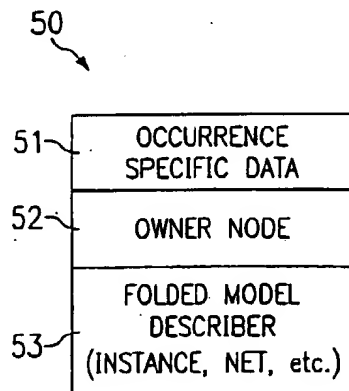


FIG. 5

Approved  
Abn  
9-2-04